



2815/4

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Pelloie, et al.

SERIAL NO.: 09/890,120

FILING DATE: July 27, 2001

TITLE: DYNAMIC THRESHOLD VOLTAGE MOS TRANSISTOR
FITTED WITH A CURRENT LIMITER, AND PROCESS FOR
MAKING THE SAME

EXAMINER: Edgardo Ortiz

ART UNIT: 2815

RECEIVED
SEP 30 2002
TECHNOLOGY CENTER 2800

CERTIFICATE OF MAILING

I hereby certify that this paper is being deposited with the United States Postal Service as First Class
Mail in an envelope addressed to: Commissioner for Patents, Washington, DC 20231, on
the date printed below:

Date: 9/19/02

Name: Diane Morse
Diane Morse

COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

TRANSMITTAL LETTER

Enclosed for filing are the following documents:

1. Request for Extension of Time; and
2. Response.

Enclosed is our check in the amount of \$110.00 for the filing fee - Fee Code 115.

The Commissioner is hereby authorized to charge our Deposit Account No. 50-1698 for
any additional fees for filing the above-noted document, including any fees required

under 37 CFR § 1.136 for any necessary Extension of Time to make the filing of the attached document timely. A duplicate copy of this page is enclosed.

Respectfully submitted,
THELEN REID & PRIEST LLP

A handwritten signature in black ink, appearing to read 'W. E. Winters', is written over a horizontal line.

William E. Winters
Reg. No. 42,232

Dated: September 18, 2002

THELEN REID & PRIEST LLP
P.O. Box 640640
San Jose, CA 95164-0640
Tel.: (408) 292-5800
Fax: (408) 287-8040



DOCKET NO.: 034299-336

UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Pelloie, et al.

SERIAL NO.: 09/890,120

FILING DATE: July 27, 2001

TITLE: DYNAMIC THRESHOLD VOLTAGE MOS TRANSISTOR
FITTED WITH A CURRENT LIMITER, AND PROCESS FOR
MAKING THE SAME

EXAMINER: Edgardo Ortiz

ART UNIT: 2815

RECEIVED
SEP 30 2002
TECHNOLOGY CENTER
#11/Electronics
10/8/02
H5

CERTIFICATE OF MAILING

I hereby certify that this paper is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to Box Non-Fee Amendment, Commissioner for Patents, Washington, DC 20231, on the date printed below:

Date:

9/19/02

Name:

Diane Morse

**BOX NON-FEE AMENDMENT
COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231**

RESPONSE

This paper is responsive to the Office Action dated August 9, 2002.

REMARKS

The present application stands subject to a restriction requirement. According to the restriction requirement, claims 1-11 have been delineated as being directed at a first invention identified as Group I drawn to a semiconductor device, classified in class 257, subclass 347 and claims 12-24 have been delineated as being directed at a second